



GeV γ -907A MIXED LOGIC

(Presented by Tohoku Univ., & Yasuo Takeda)

General

This module is Octal independent Fan-in Fan-out and designed for Negative logic input.

Threshold voltage (simplified discr) and OR function can be set by switches.

OR function is easily determined by front panel rotary switch.

Threshold voltage is determined by rear panel code switch.

Specification

Propagation Delay time : <10ns

Maximum rate : >100MHz

Minimum pulse width : <10ns

Input / Output NIM single

Impedance 50 Ω

- 2-input x 8
- 2-output(left side output) x 8 :output for each channel input
- 2-OR output(right side output) x 8 :OR-output can be change for 2,4,8 input
- 2-OR16-output x 1 :OR16-output for all input
- 1-Veto input x 1 :leading edge type

OR function	Octal	(2in-4out)x8 (OR16-out)x2
	Quad	(2in-2out)x8 (4in-4out)x4 (OR16-out)x2
	Dual	(2in-2out)x8 (8in-8out)x2 (OR16-out)x2
Threshold voltage	50mV, 100mV, 150mV, 200mV, 250mV, 300mV, 350mV, 400mV, 450mV, 500mV	

Packaging NIM#1 with LEMO 00 connectors.

Power requirements +6V 1.1A -6V 1.2A

Wight 2kgs

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